

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A method of preventing the external detection of operations in a digital integrated circuit comprising an asynchronous circuit, comprising:

 time-varying a supply voltage of said asynchronous circuit, by a random number generator,
to time-shift the execution time of operations within said asynchronous circuit,

 wherein the time variation of said supply voltage takes place in a random way.
2. (Canceled)
3. (Previously Presented) A digital integrated circuit comprising:

 an asynchronous circuit, and

 means for time-varying a supply voltage of said asynchronous circuit to time-shift the execution point of operations within said asynchronous circuit,

 wherein said means for time-varying said supply voltage comprises a random number generator.
4. (Canceled)
5. (Previously Presented) The digital integrated circuit according to claim 3, wherein said means for time-varying said supply voltage further comprises a noise voltage source driving said random-number generator.
6. (Previously Presented) The digital integrated circuit according to claim 3, wherein said means for time-varying said supply voltage further comprises a digital-analog converter transforming the digital values produced by said random-number generator into an analog voltage.
7. (Original) The digital integrated circuit according to claim 3, wherein said means for time-varying said supply voltage further comprises a voltage regulator.

8. (Original) The digital integrated circuit according to claim 3, wherein said asynchronous circuit is formed for executing a coding algorithm.

9. (Previously Presented) The method according to claim 1, wherein the asynchronous circuit is a type which performs processing without correlation to a clock.

10. (Previously Presented) The digital integrated circuit according to claim 3, wherein the asynchronous circuit is a type which performs processing without correlation to a clock.